#### (1) Remarks

Dear Examiner,

In the Final Office Action dated 09/01/2005, page 5 in remarks regarding our Affidavit of prior invention, an argument was made that there was insufficient evidence supporting diligence in reducing the invention to practice, between our last recorded diligence date of September 5, 2000, and the initial provisional filing date of October 9, 2002. In particular it was stated that further evidence is needed to establish diligence. In this response, we set out a clear recorded timeline of diligence between the dates of September 5, 2000 and October 9, 2002. Between those two dates many variations of the spot size converter were simulated, fabricated, and measured. We provide recorded highlights of those events.

With the evidence provided in the following continuation of the declaration of prior invention, we now maintain that the amended claims in our response to the First Office Action represent patentable novel structure over the cited prior art.

### (3) Declaration filed under section 1.131

We request that the examiner take into account the following continuation of our affidavit of prior invention in our response to the First Office Action, pursuant to section 1.131.

#### Inventor's credentials

Dr. Little obtained a B.A.Sc. in Electrical Engineering in 1984, and a PhD in Electrical Engineering in 1994, both from the University of Waterloo, Canada. He held a Post Doctoral position, and subsequently a Research Associate position, at the Massachusetts Institute of Technology (MIT) for five years. He has held a Research Professor position and the University of Maryland, as well as positions at Nortel Networks and Fujitsu. He has consulted for a number fiber optic companies. In 2000, he founded Little Optics Inc, and served as its President and Chief Technology Officer. The company has since raised \$25M in venture capital. Little Optics Inc pioneered high index contrast photonic circuits. Dr. Little has published over 75 articles in peer reviewed journals and holds several patents in the field.

### Place of Invention

The work performed in conceiving, analyzing, and reducing to practice of this invention was performed in the USA.

The following pages and Exhibits from my lab notebook and from a commercial photomask order, combined with those set forth in our response to the First Office Action, detail this invention as occurring before the priority dates of the cited prior art references (Appl. No. 10/006,752 to Lam et. al. and Appl. No. 10/083,674 to Zhou et. al.). They also set forth a timeline of diligence between the date of first documentation of invention and first filing of application, in reducing the invention to practice.

I declare that the data, devices and products of this application were conceived by me on Brent Little

the dates listed.

In our Response to First Office Action, we set forth our initial conception date of May 25, 2000. Between the initially recorded conception date of May 25, 2000, and our priority provisional application date of October 9, 2002 for this application, there was a continuity of diligent activity in analyzing and reducing the spot size converter to practice. The following recorded activities took place between those two dates:

- (1) Exhibit A is a copy of page 35 of Brent Little's "Little Optics Lab Notebook #3" showing simulations of a spot size transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. The recording of these simulations is November 12, 2000.
- (2) Exhibit B is a copy of page 37 of Brent Little's "Little Optics Lab Notebook #3" showing simulations of a spot size transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. This mode transformer is similar to the one of this application, but has the smaller high confinement waveguide buried within the larger waveguide, rather than on top of. The recording of these simulations is December 9, 2000.
- (3) Exhibit C is a copy of page 62 of Brent Little's "Little Optics Lab Notebook #3" showing more simulations of a spot size mode transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. This mode transformer is similar to the one of this application, but has the smaller high confinement waveguide buried within the larger waveguide, rather than on top of. The recording of these simulations is August 15, 2001.
- (4) Exhibit D is a copy of page 77 of Brent Little's "Little Optics Lab Notebook #3" showing more simulations of a spot size mode transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. This mode transformer is similar to the one of this application, but has the smaller high confinement waveguide buried within the larger waveguide, rather than on top of. The recording of these simulations is October 6, 2001.

- (5) Exhibit E is a copy of page 12 of Brent Little's "Little Optics Lab Notebook #4" showing sketches of photomask layout used to fabricate and quantify mode transformer performance. The recording of these simulations is February 24, 2002.
- (6) Exhibit F is a copy of page 110 of Brent Little's "Little Optics Lab Notebook #3" showing simulations of a spot size mode transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide. This mode transformer is similar to the one of this application. The recording of these simulations is March 2, 2002.
- (7) Exhibit G is a copy of page 13 of Brent Little's "Little Optics Lab Notebook #4" showing simulations for a variation of the spot size mode transformer. This mode transformer is similar to the one of this application. The recording of these simulations is April 2, 2002.
- (8) Exhibit H is a copy of page 14 of Brent Little's "Little Optics Lab Notebook #4" showing measured data of a fabricated device for the coupling efficiency of coupling from a fiber into a fabricated large waveguide portion of the mode transformer. The recording of these measurements is April 11, 2002.
- (9) Exhibit I is a copy of page 20 of Brent Little's "Little Optics Lab Notebook #4" showing scanning electron microscope (SEM) images of a cross section of a fabricated variation of the spot size mode transformer having the small waveguide imbedded inside the large waveguide. The observed "seams' in the fabrication indicated that the small waveguide should be put adjacent to (below or on top) of the large waveguide as originally proposed in the lab book notes of May 25, 2000, August 22, 2000, and September 5, 200. The recording of these measurements is April 25, 2002.
- (10) Exhibit J is a copy of page 28 of Brent Little's "Little Optics Lab Notebook #4" showing simulations of a variation of the present mode transformer having more than two sections. The recording of these simulations is June 1, 2002.

(11) Exhibit K is a copy of a purchase order sent to DuPont Photomasks for the spot size converter of Exhibit F. The purchase order is for a set of photomask reticles that was subsequently used to fabricate the structure of Exhibit F.

### **Conditional Request for Constructive Assistance**

Applicant has amended the specification and claims of this application so that they are proper, definite and define novel structure which is also unobvious. If, for any reason this application is not believed to be in full condition for allowance, applicant respectfully requests the constructive assistance and suggestions of the Examiner pursuant to M.P.E.P. section 2173.02 and section 707.07(j) in order that the undersigned can place this application in allowable condition as soon as possible and without the need for further proceedings.

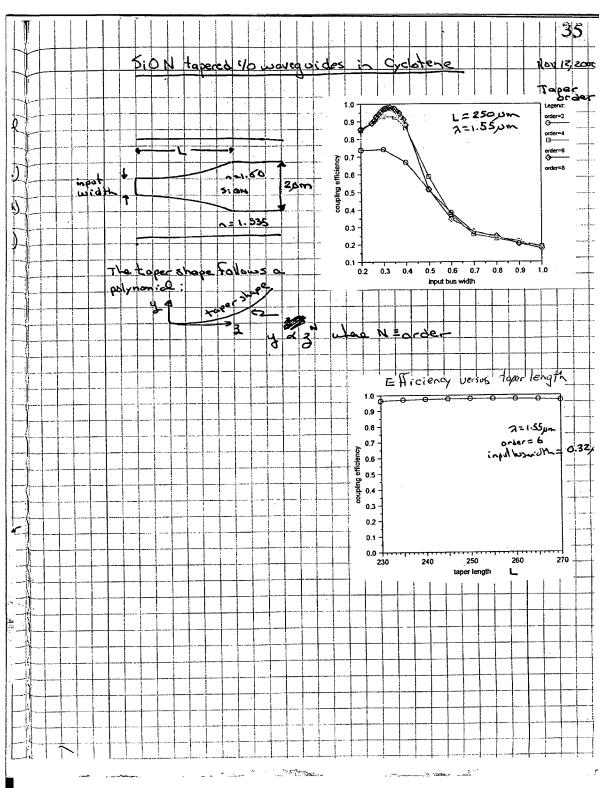
Very Respectively,

Brent E. Little

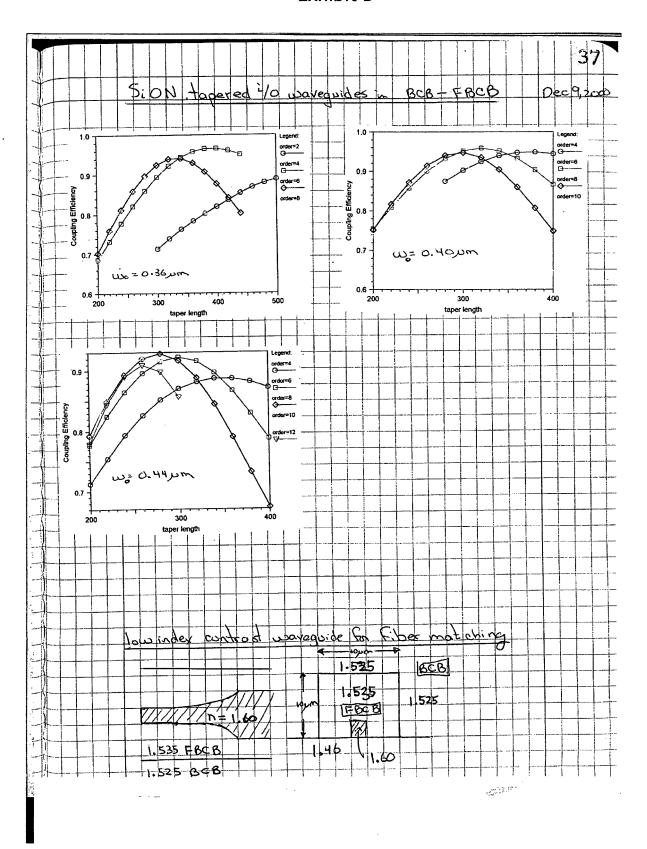
Applicant Pro Se



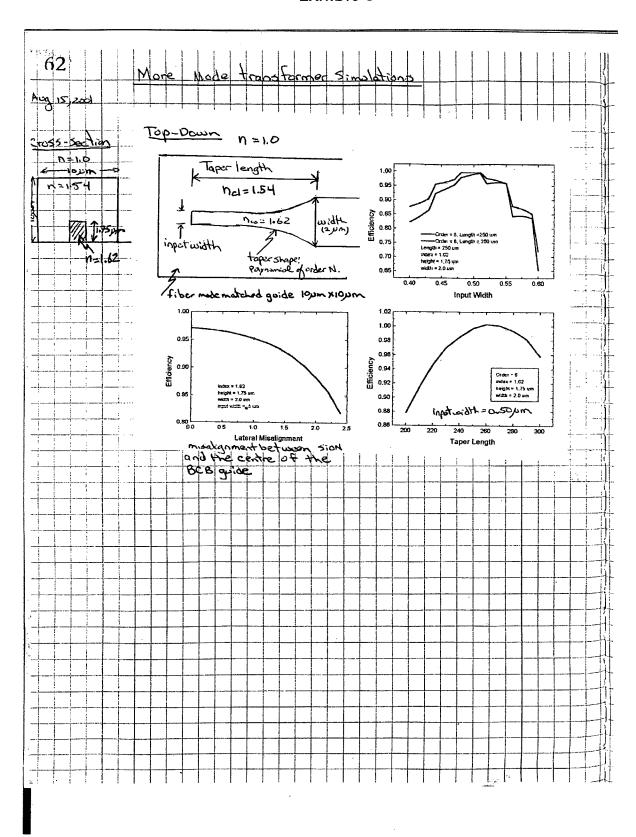
### Exhibit A



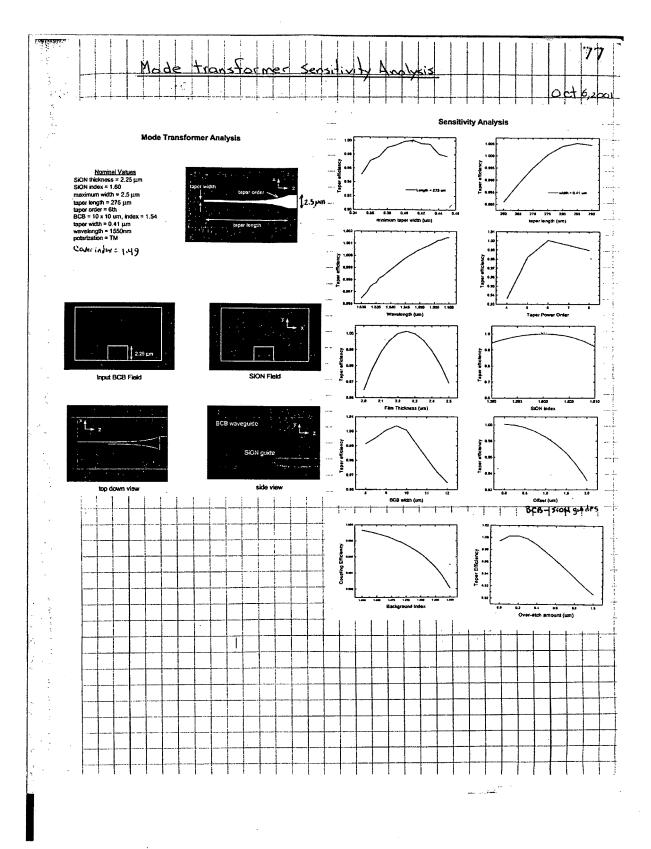
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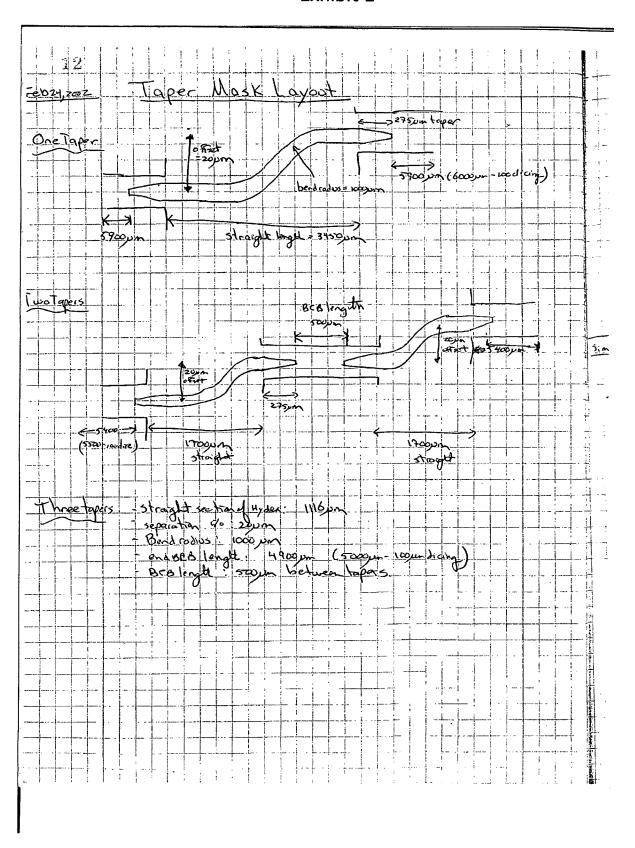
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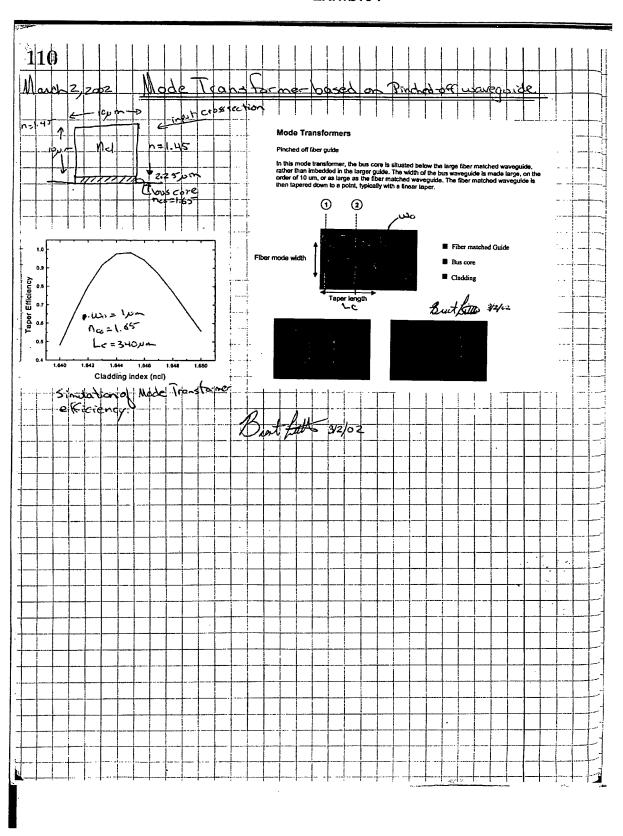
# Exhibit D



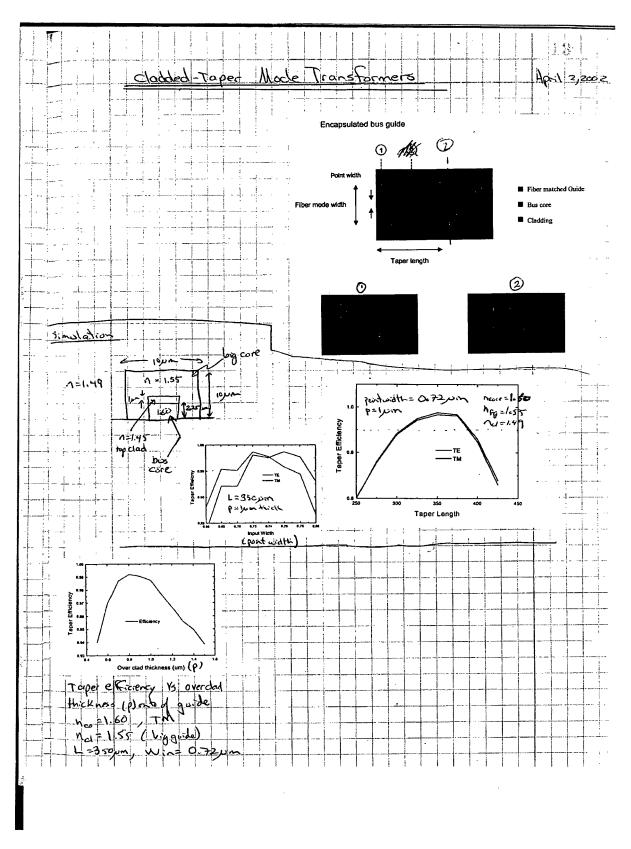
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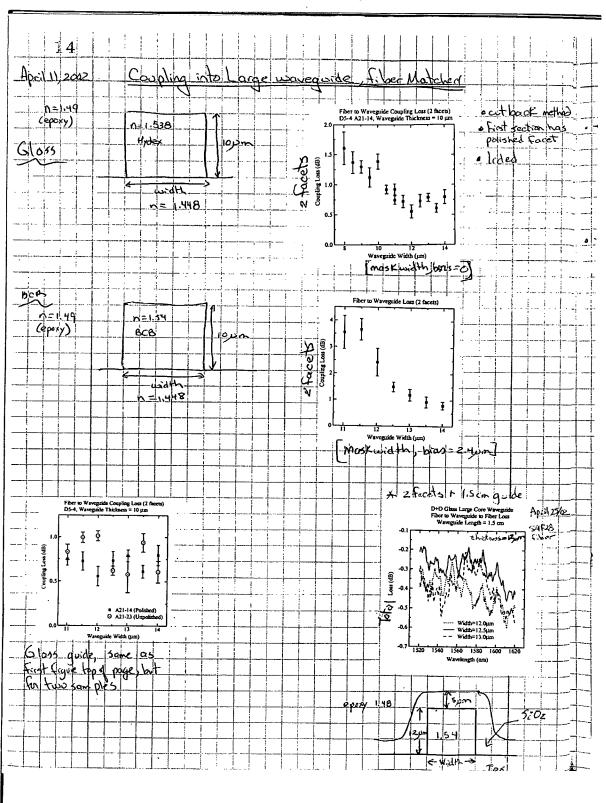
# Exhibit F



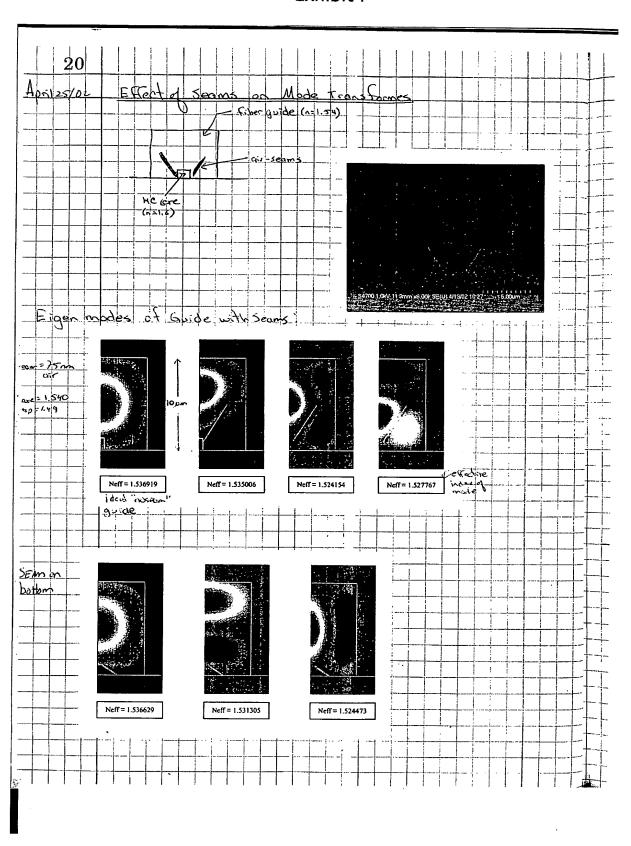
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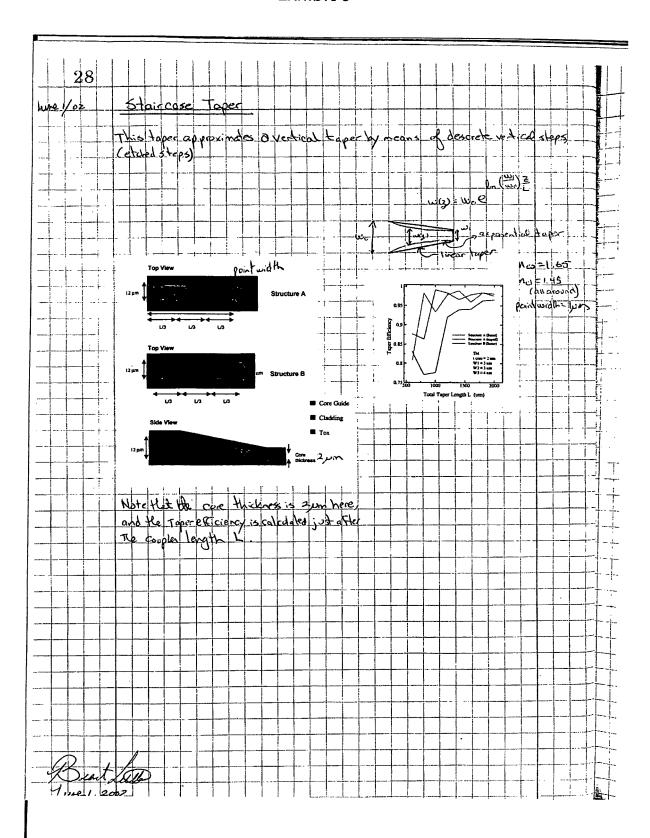
## Exhibit H



# Exhibit I



# Exhibit J



### Exhibit K

